

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7584	driver near3 circuit.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:31
L2	0	L1 and "372"/26.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:32
L3	14	L1 and "372"/26.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:33
L4	977	"372"/26.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:33
L5	514	4 AND circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:33
L6	202	4 AND driv\$3 near3 circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:49
L7	1	(driver and circuit and bipolar near3 transistor\$1 and current near3 source and reference near3 voltage and capacitor and impedance and differential and supply near3 voltage and ac).clm.	US-PGPUB	OR	OFF	2005/09/04 12:51
S1	0	(eom or (electro near3 optical near3 modulat\$3)) and driv\$3 and trasistor\$1 and capacitor\$1 and current near3 source\$1 and voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 16:20
S2	67	(eom or (electro near3 optical near3 modulat\$3)) and driv\$3 and transistor\$1 and capacitor\$1 and current near3 source\$1 and voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 16:23

S3	20925	driv\$3 near3 circuit\$1 and transistor\$1 and capacitor\$1 and current near3 source\$1 and voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 16:22
S4	4259	driv\$3 near3 circuit\$1 and transistor\$1 with differential and capacitor\$1 and current near3 source\$1 and voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 16:23
S5	325	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:05
S6	1	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and fast near3 swing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:07
S7	155	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and high near3 speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:09
S8	0	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and fast near3 swing and giga near3 bit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:08
S9	0	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and high near3 speed and giga near3 bit\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:08
S10	0	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and high near3 speed and gbs	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:08
S11	156	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and ((high near3 speed) or gb/s)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:14
S12	6	driv\$3 near3 circuit\$1 and ((bipolar near3 transistor\$1) with differential) and capacitor\$1 and current near3 source\$1 and voltage\$1 and gb/s	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:10

S13	150	S7 not S12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/01 17:14
S14	2	driver near3 circuit and bias near3 t near3 network	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 08:41
S15	0	driver near3 highe near3 speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 08:41
S16	2597	driver near3 high near3 speed	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 08:41
S17	151	driver near3 high near3 speed and differential near3 transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 09:33
S18	7578	driver near3 circuit.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S19	106	S18 and modulator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 09:54
S20	74	driver near3 modulator.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 09:54
S21	52	S20 not S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 09:58
S22	647	S18 and "327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:07

S23	7	S18 and "359"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S24	75	S18 and "372"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/04 12:31
S25	3	S18 and "320"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06
S26	1	S18 and "332"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06
S27	126	S18 and "361"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06
S28	82053	driver near3 circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S29	5337	S28 and "327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S30	924	S28 and "359"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S31	692	S28 and "372"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:05
S32	239	S28 and "320"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06

S33	112	S28 and "332"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06
S34	2027	S28 and "361"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:06
S35	1042	S29 and transistor\$1 with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:28
S36	647	S18 and "327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:28
S37	90	S18 and "327"/\$.ccls. and transistor\$1 with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:39
S38	0	S18 and "320"/\$.ccls. and transistor\$1 with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:38
S39	2	S18 and "320"/\$.ccls. and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:39
S40	3	S18 and "320"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:39
S41	1	S18 and "332"/\$.ccls. and transistor\$1 with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:48
S42	1	S18 and "332"/\$.ccls. and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:40

S43	1	S18 and "332"/*.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:41
S44	112	driver near3 circuit\$1 and "332"/*.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:40
S45	10	S18 and "361"/*.ccls. and transistor\$1 with differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:48
S46	98	S18 and "361"/*.ccls. and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 10:59
S47	51	S18 and "372"/*.ccls. and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:07
S48	283	372/12.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:31
S49	38	S48 and driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:31
S50	977	372/26.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:48
S51	172	S50 and driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:32
S52	574	372/38.02.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:48

S53	298	S52 and driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:48
S54	182	S52 and driver and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:49
S55	188	S52 and driver and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:59
S56	136	327/65.ccls. and driver and transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/02 11:59